**Name of UNIOS University Unit: Faculty of Electrical Engineering,**

**Computer Science and Information Technology**

**COURSES OFFERED IN FOREIGN LANGUAGE**

**FOR ERASMUS+ INDIVIDUAL INCOMING STUDENTS**

|  |  |
| --- | --- |
| **Department or Chair within the UNIOS Unit**  | **Department of Computer Engineering and Automation** |

|  |  |
| --- | --- |
| **Study program**  | **Bachelor Degree in Computer Engineering** |

|  |  |
| --- | --- |
| **Study level** | **1st cycle** |

|  |  |
| --- | --- |
| **Course title** | **Digital Electronics** |
| **Course code (if any)** | **PR201-20** |
| **Language of instruction** | **English** |
| **Brief course description** | **Number systems and code. Logic functions, combinatorial and sequential logic. Minimizations and state diagrams. Bool algebra. Programable logic devices, FPGA, VHDL. Design and test of logic devices with VHDL and FPGA.** |
| **Form of teaching** | **Lectures are optional, laboratory practice is obligatory** |
| **Form of assessment** | **Successful completion of laboratory practice, tests and oral examination.** |
| **Number of ECTS** | **6** |
| **Class hours per week** | **5** |
| **Minimum number of students** | **1** |
| **Period of realization**  | **Summer semester** |
| **Lecturer** | **Tomislav Matić, PhD, Assistant Professor** |